

FIG. 1

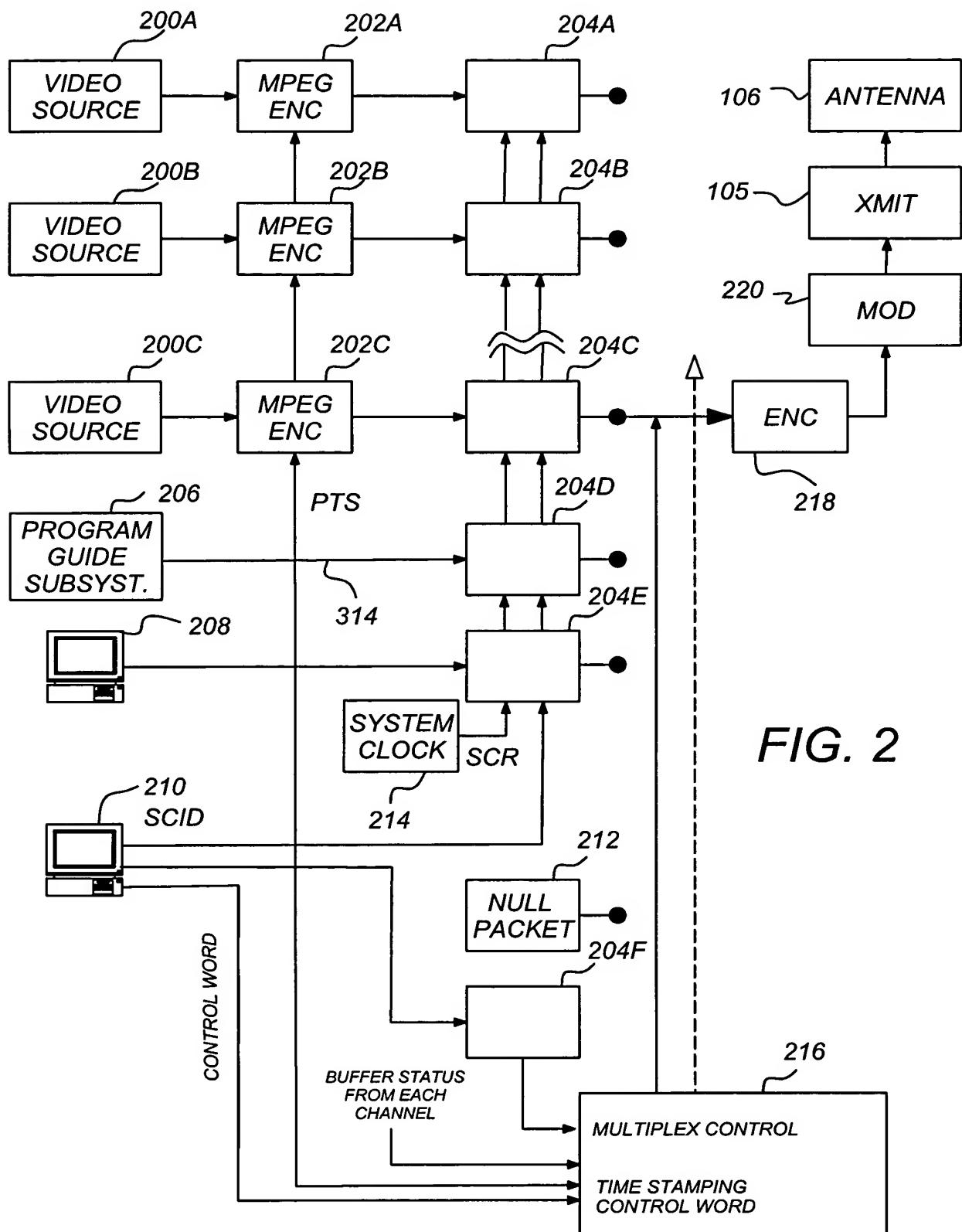


FIG. 2

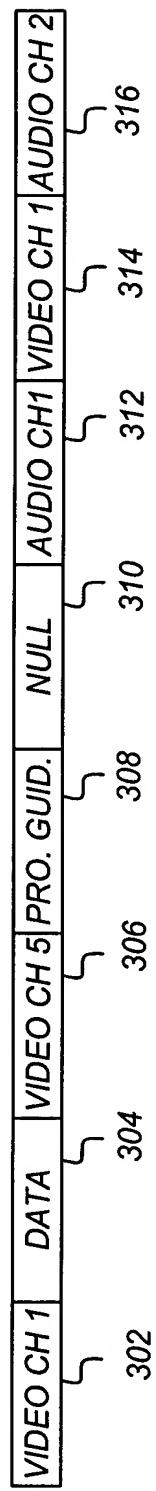


FIG. 3A

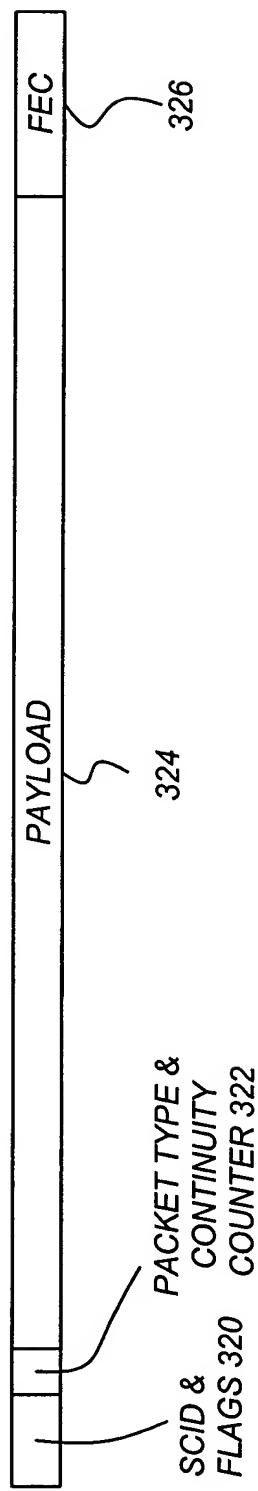


FIG. 3B

220
↓

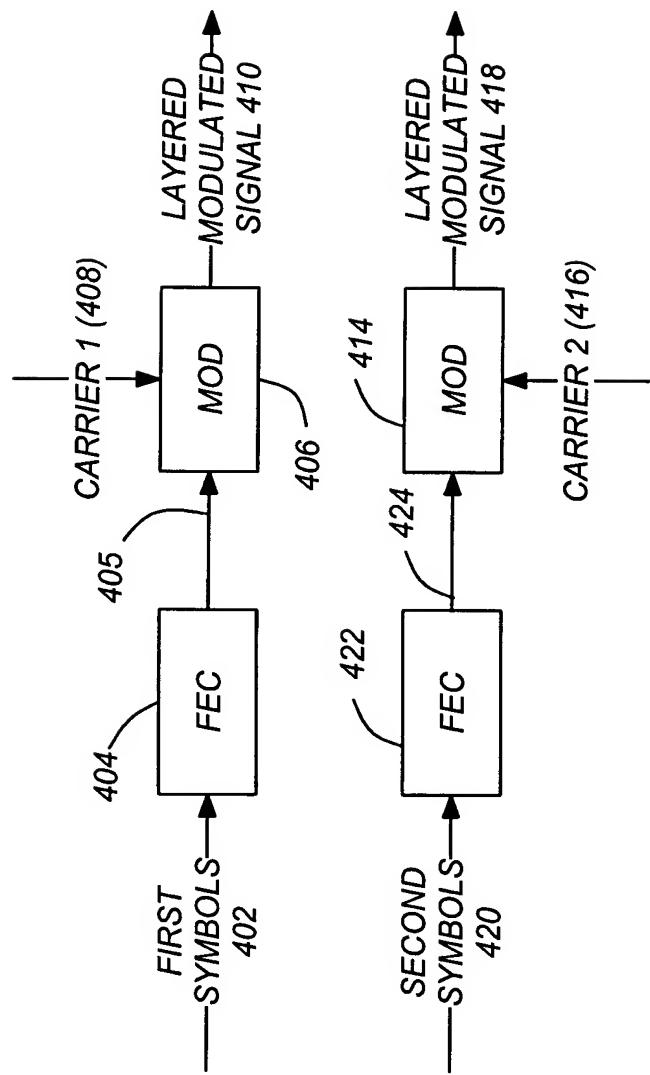


FIG. 4

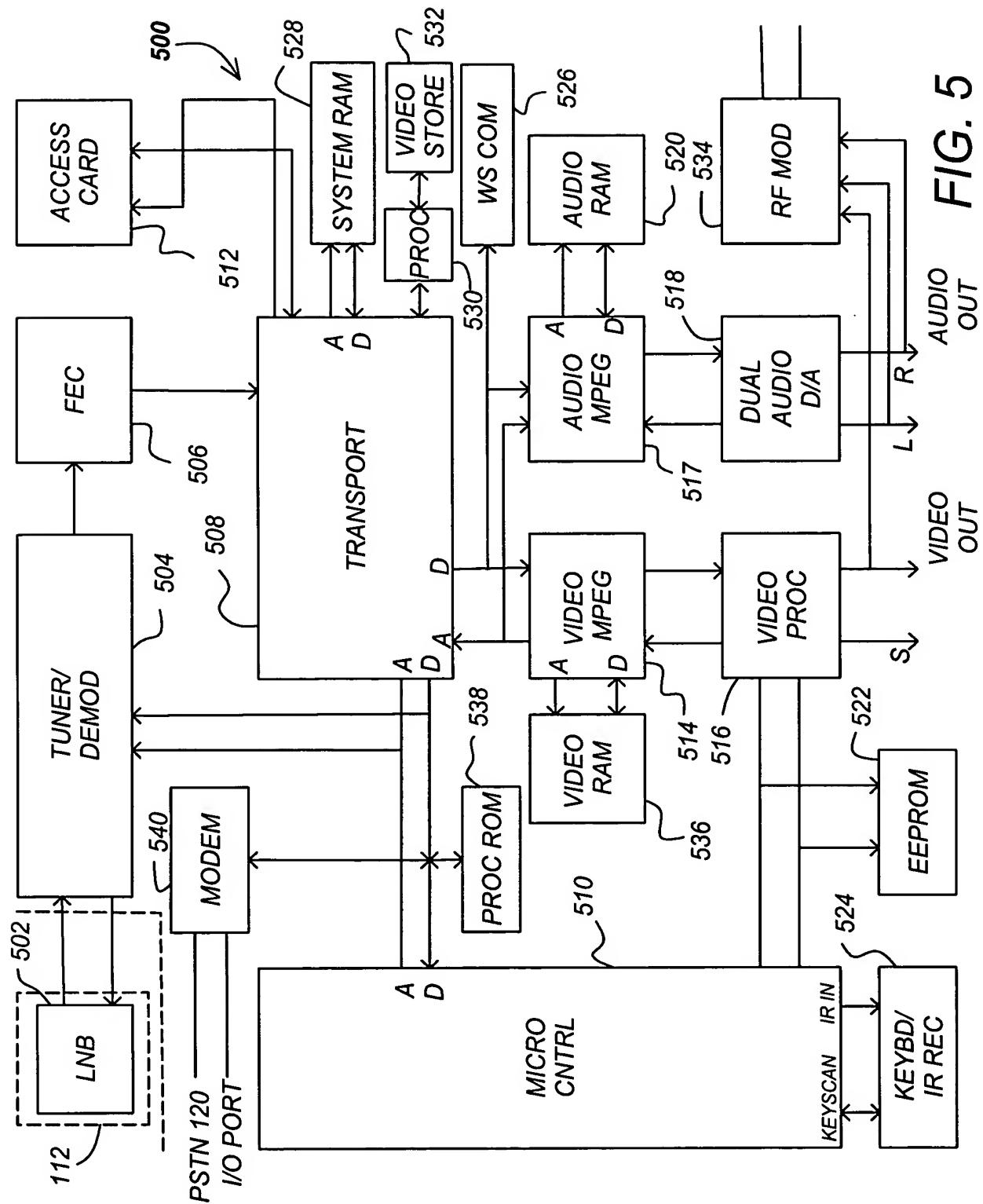


FIG. 5

FIG. 6B

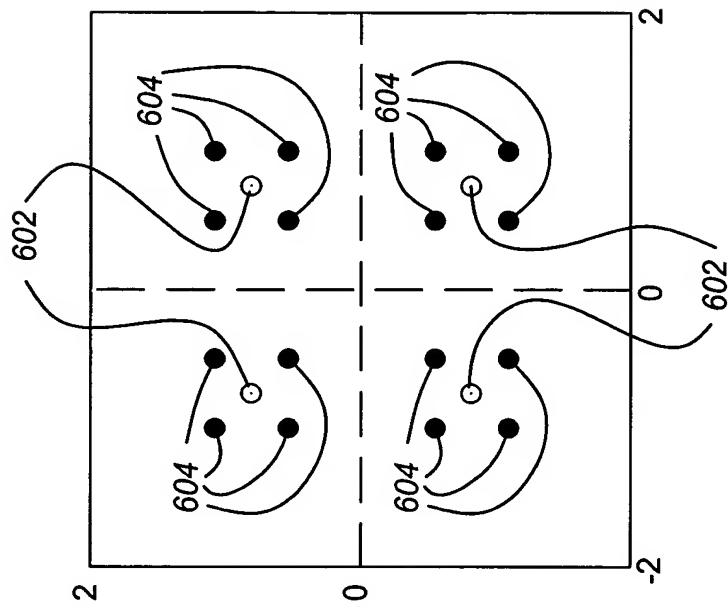
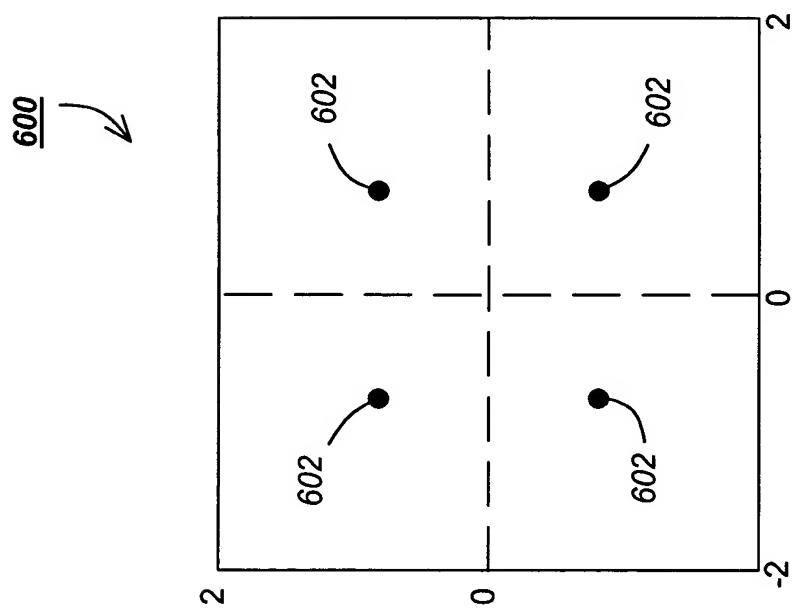


FIG. 6A



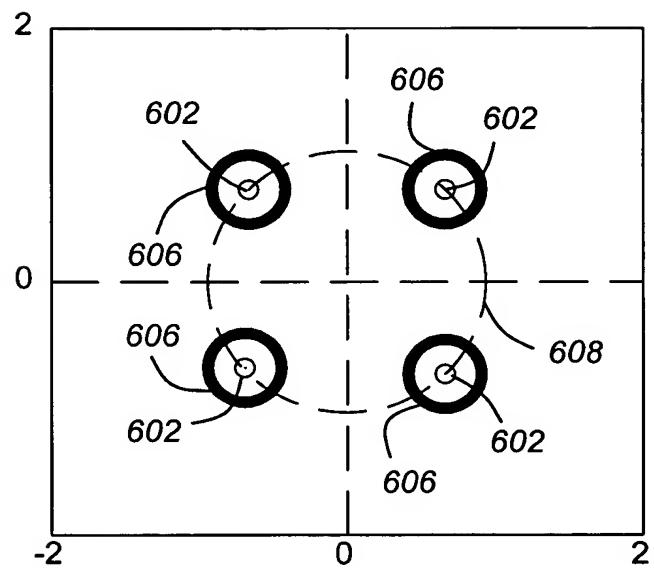


FIG. 6C

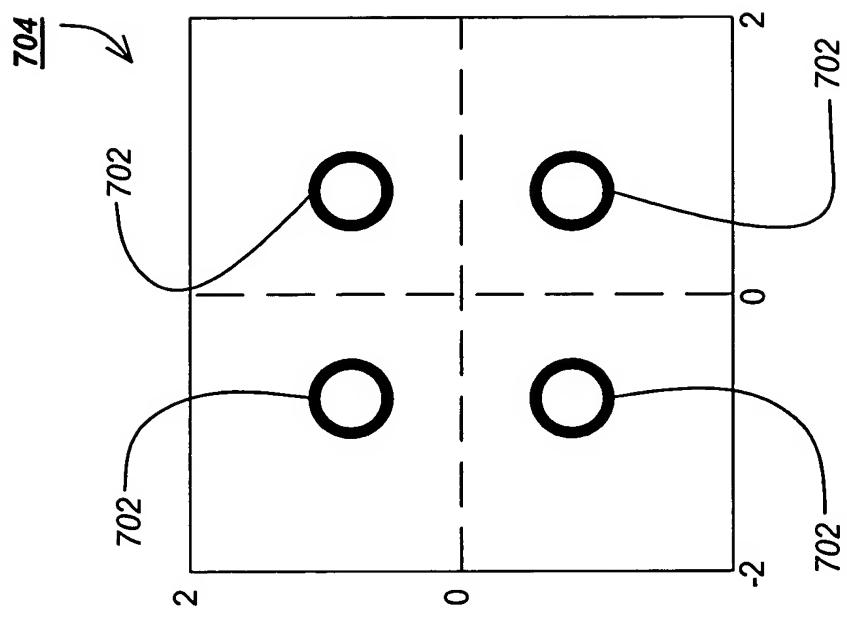


FIG. 7B

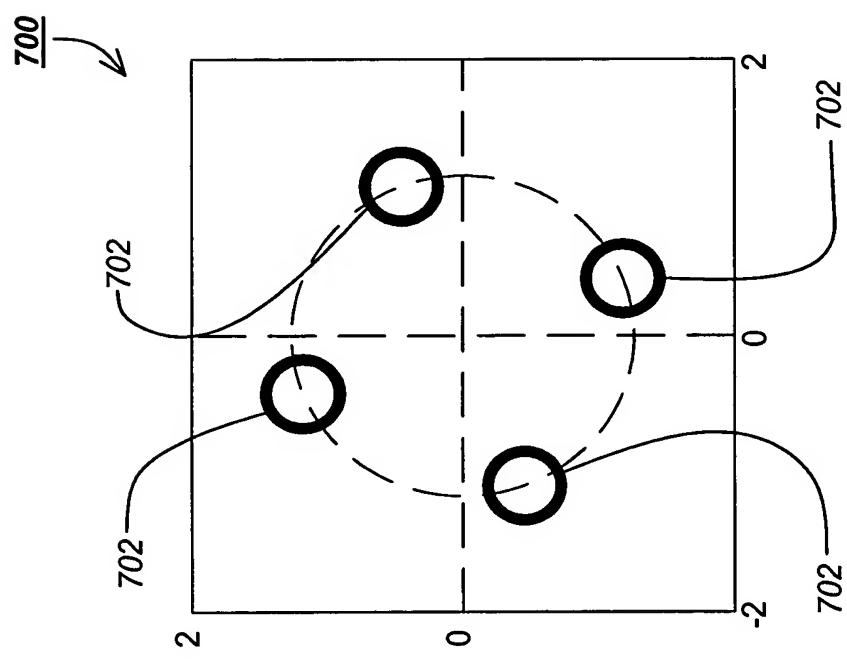


FIG. 7A

Phase Error Distribution

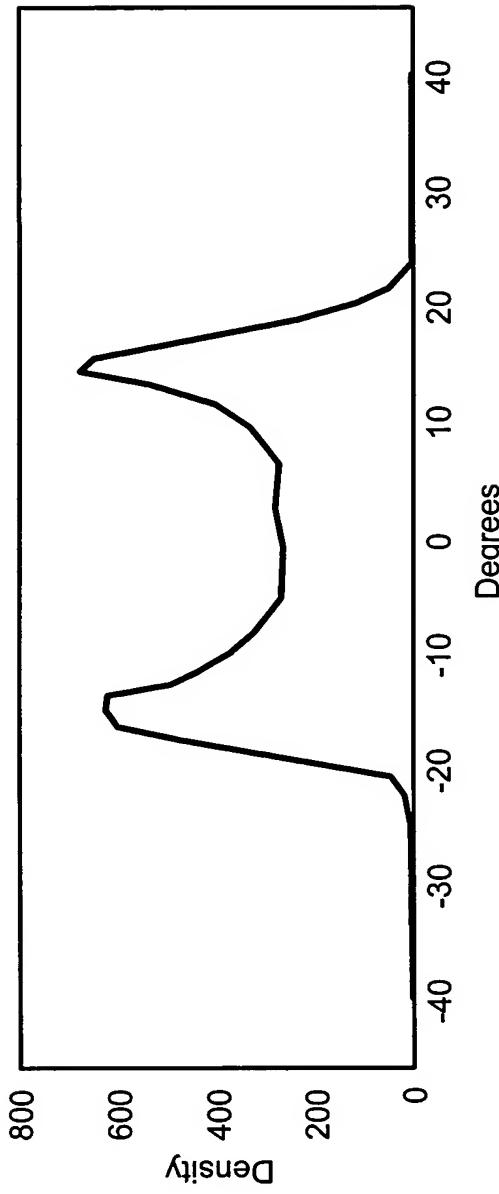


FIG. 7C

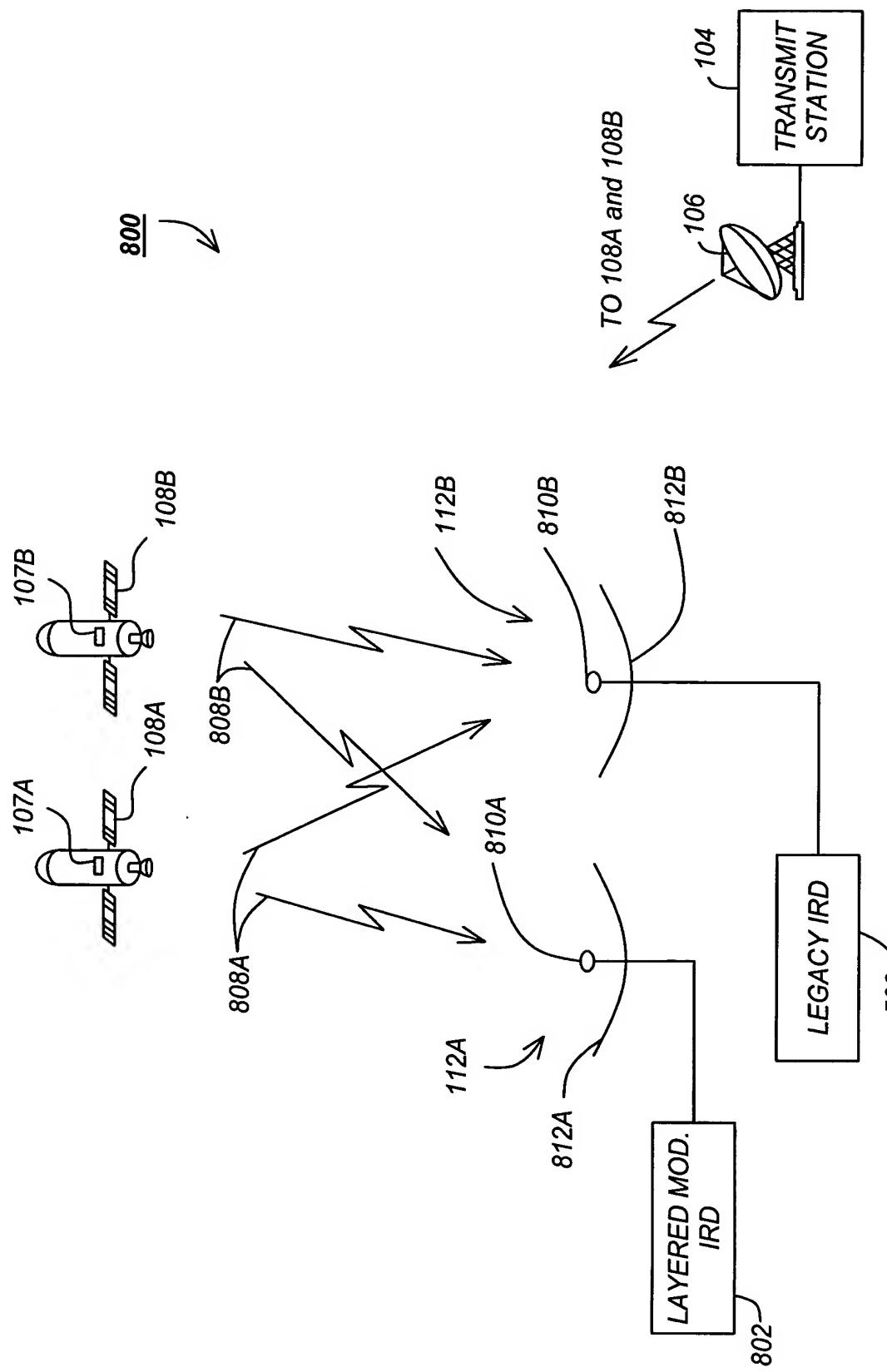


FIG. 8A

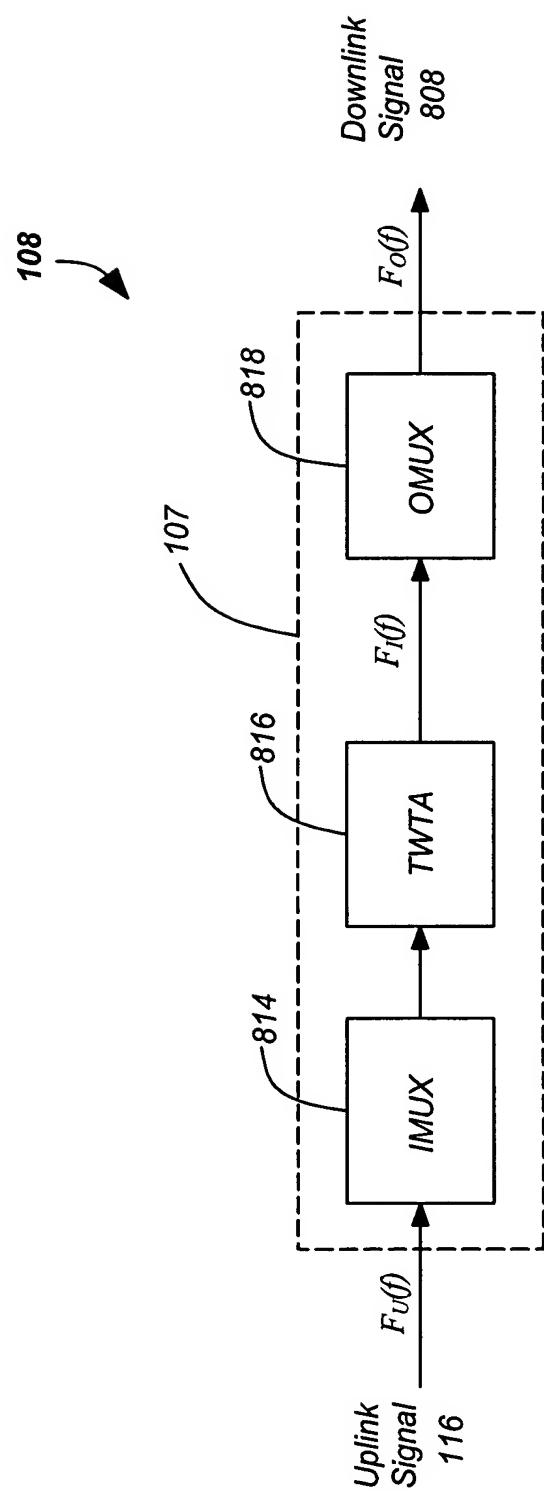


FIG. 8B

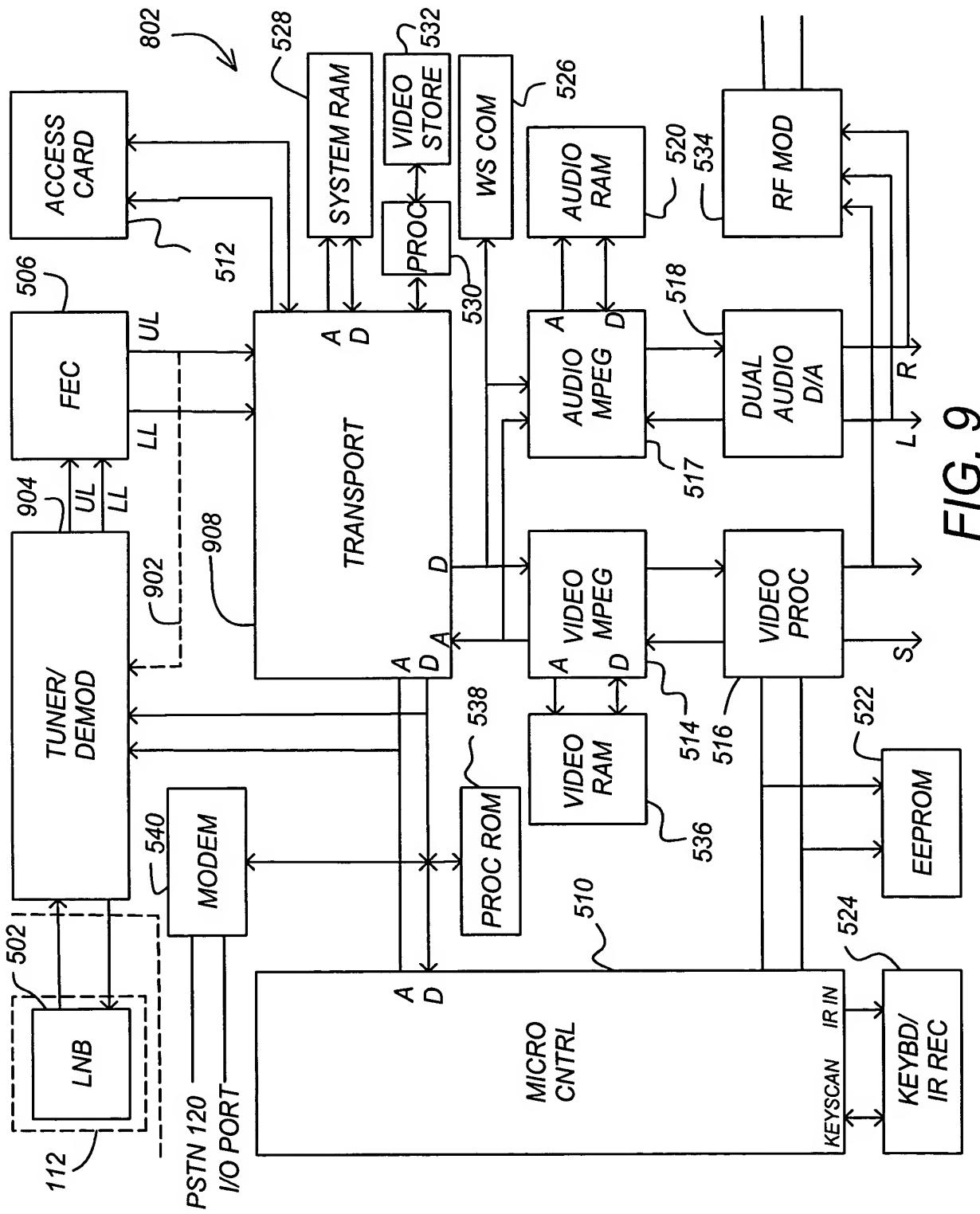


FIG. 9

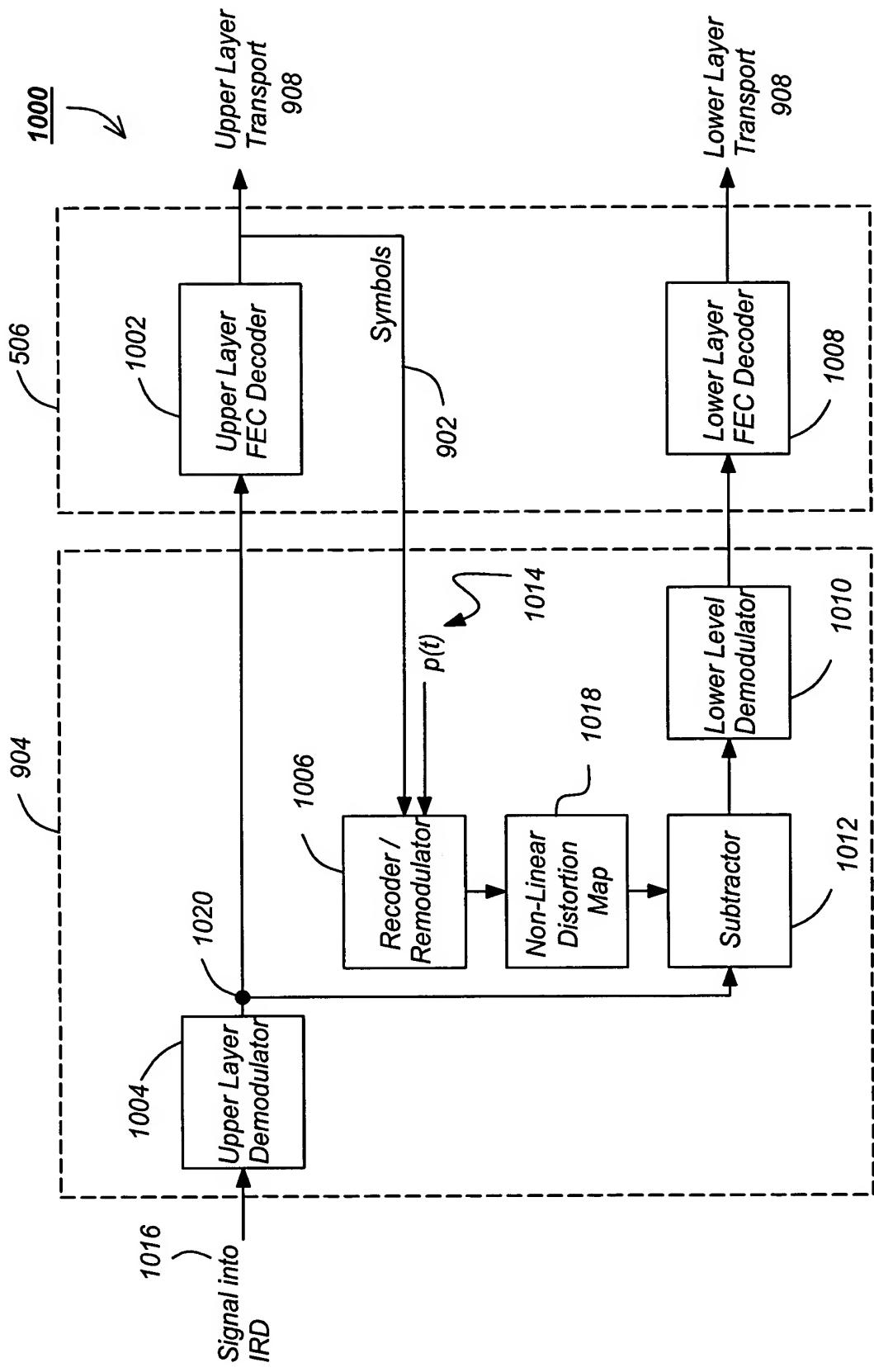


FIG. 10A

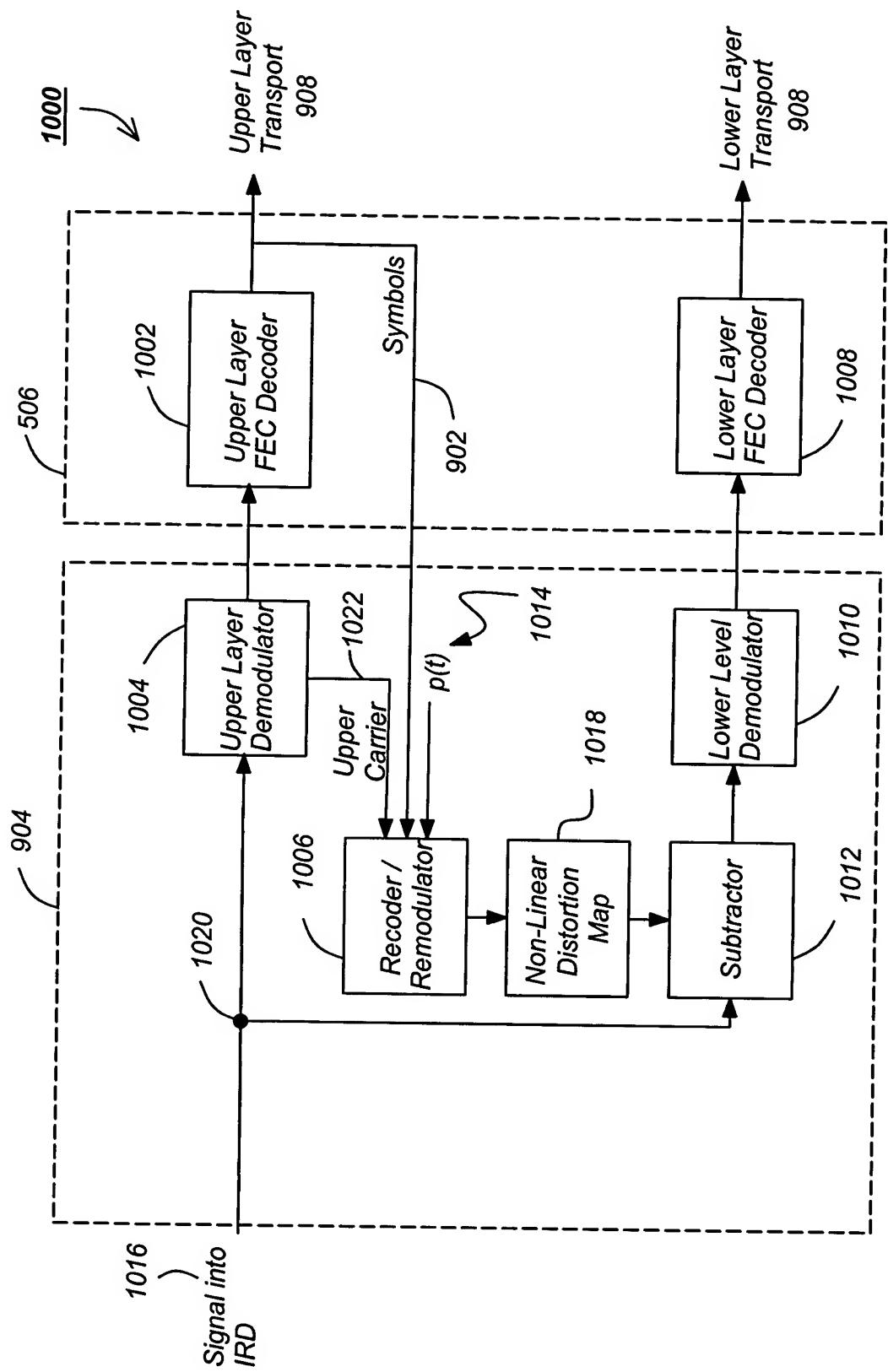


FIG. 10B

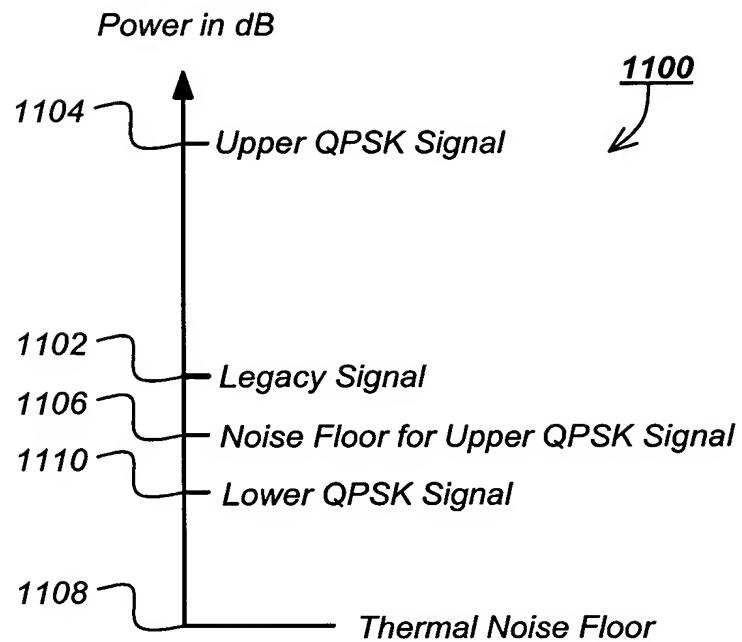


FIG. 11A

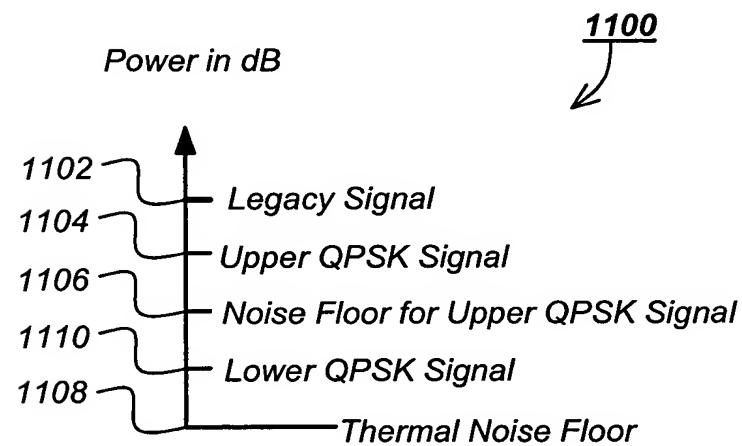


FIG. 11B

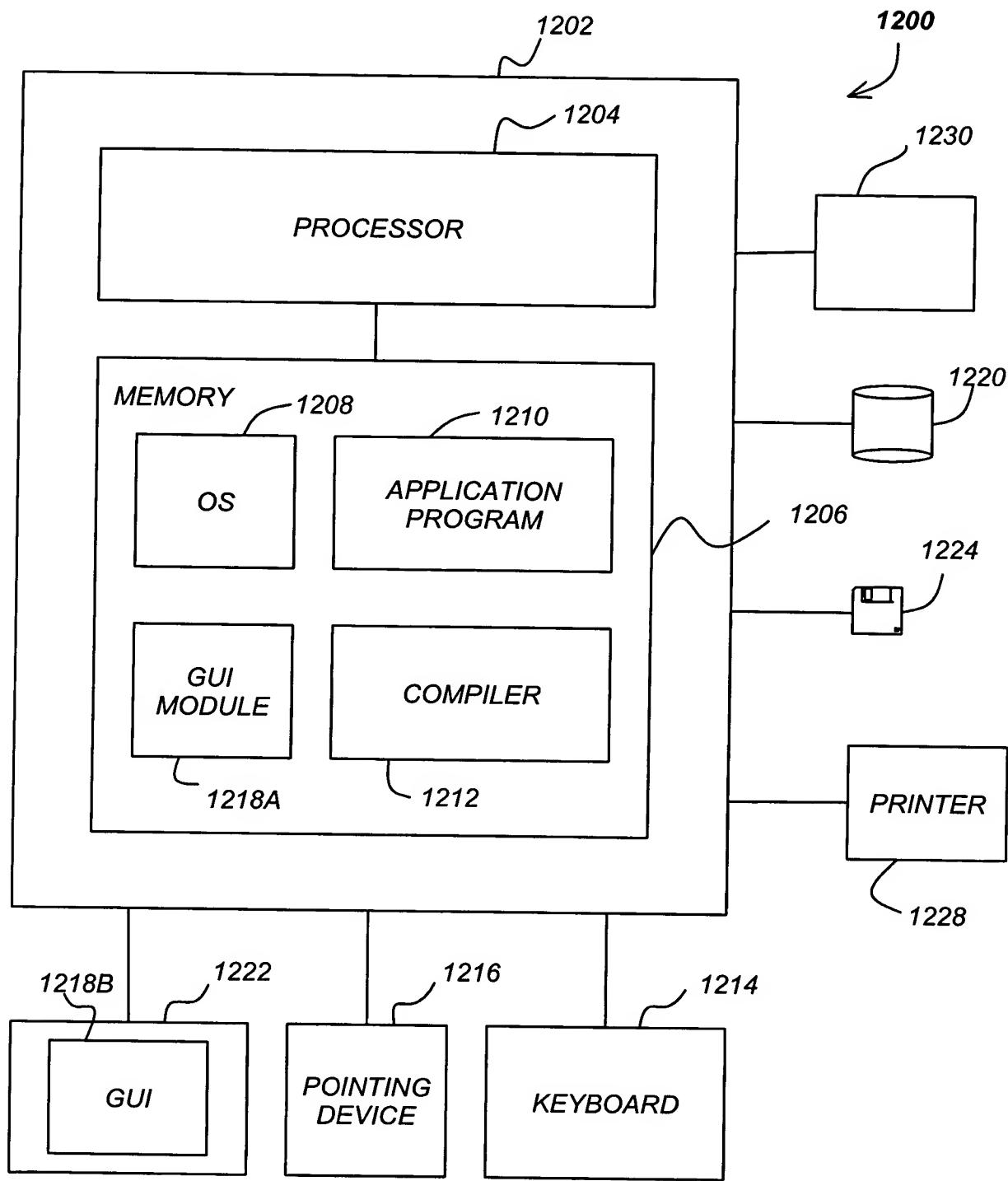


FIG. 12

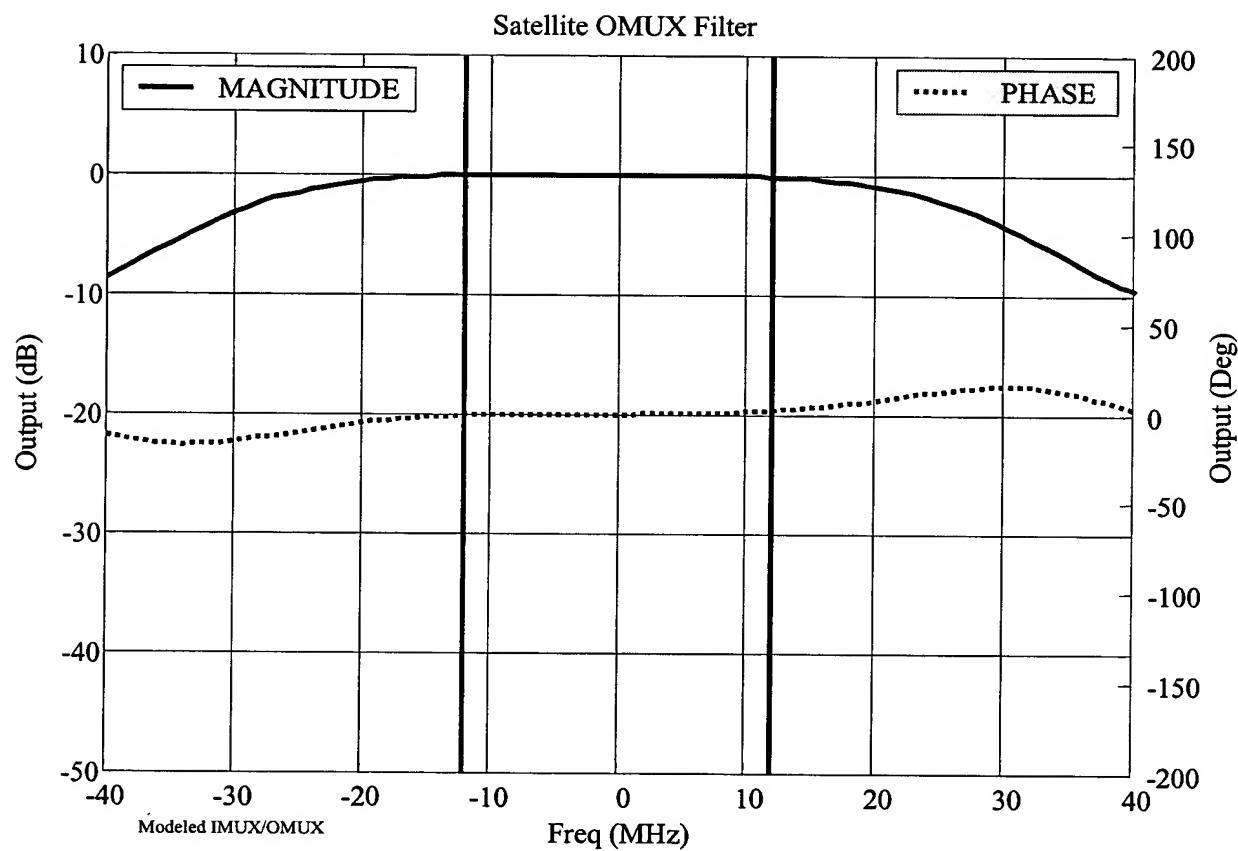


FIG. 13

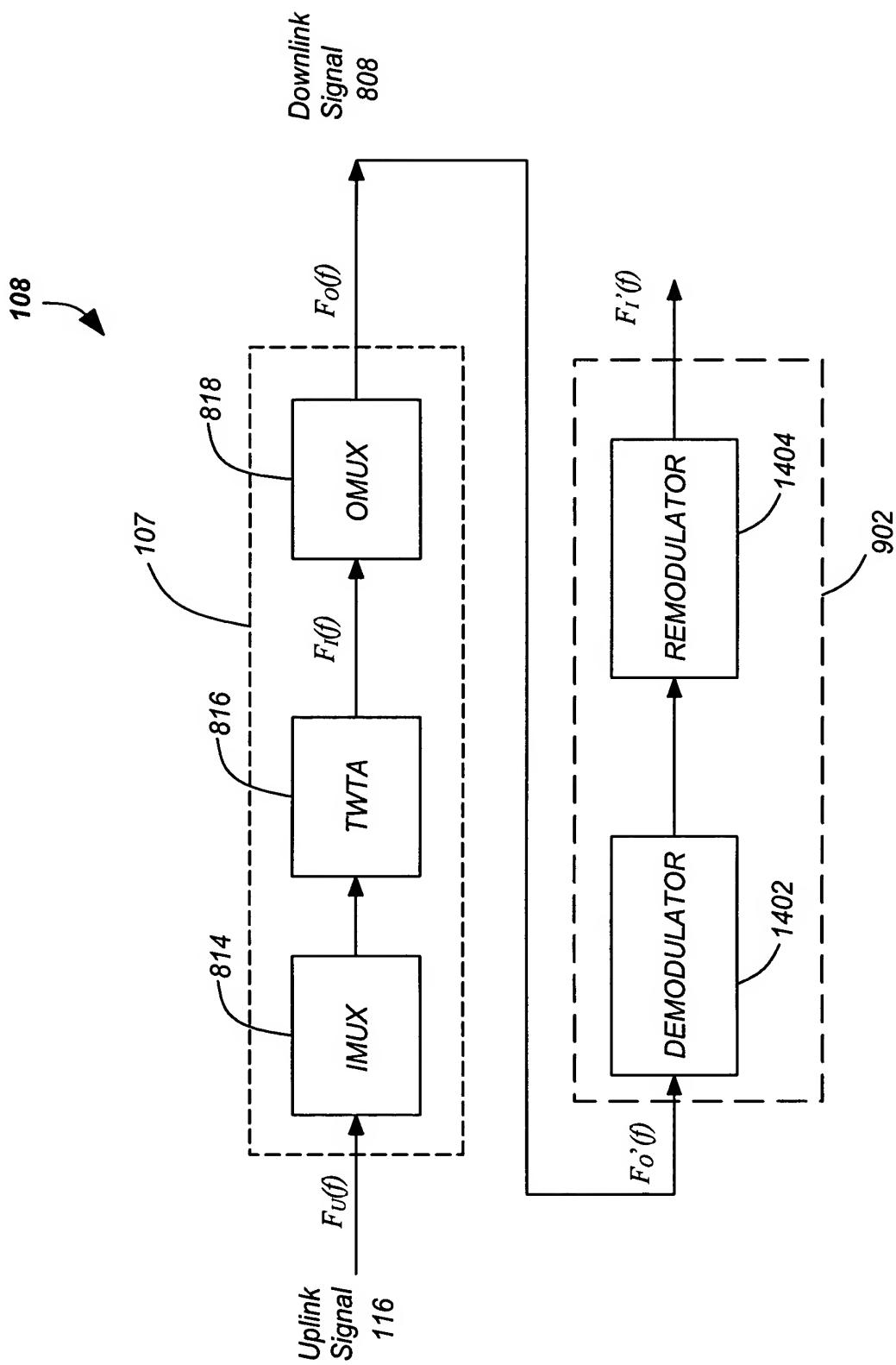


FIG. 14

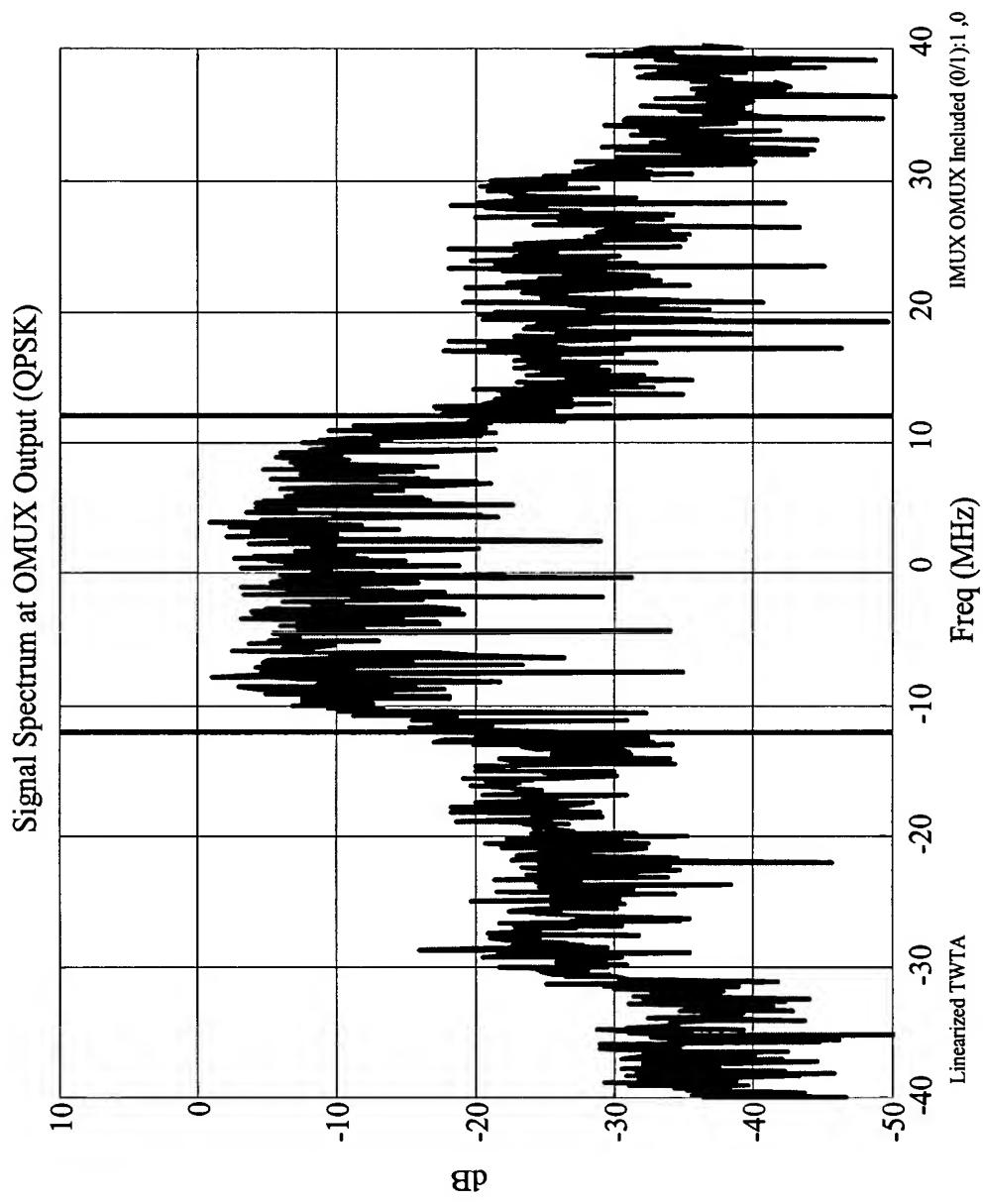


FIG. 15A

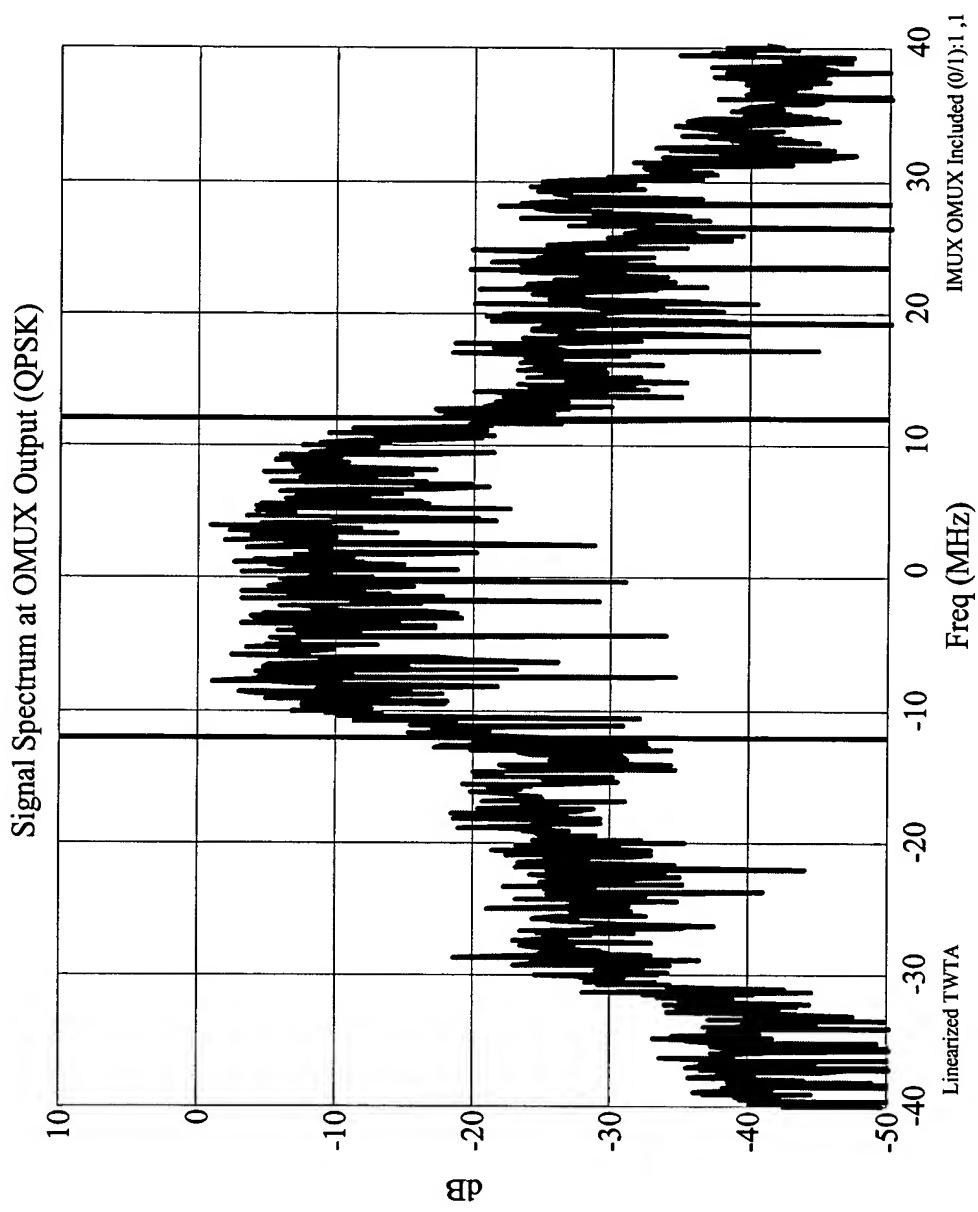


FIG. 15B

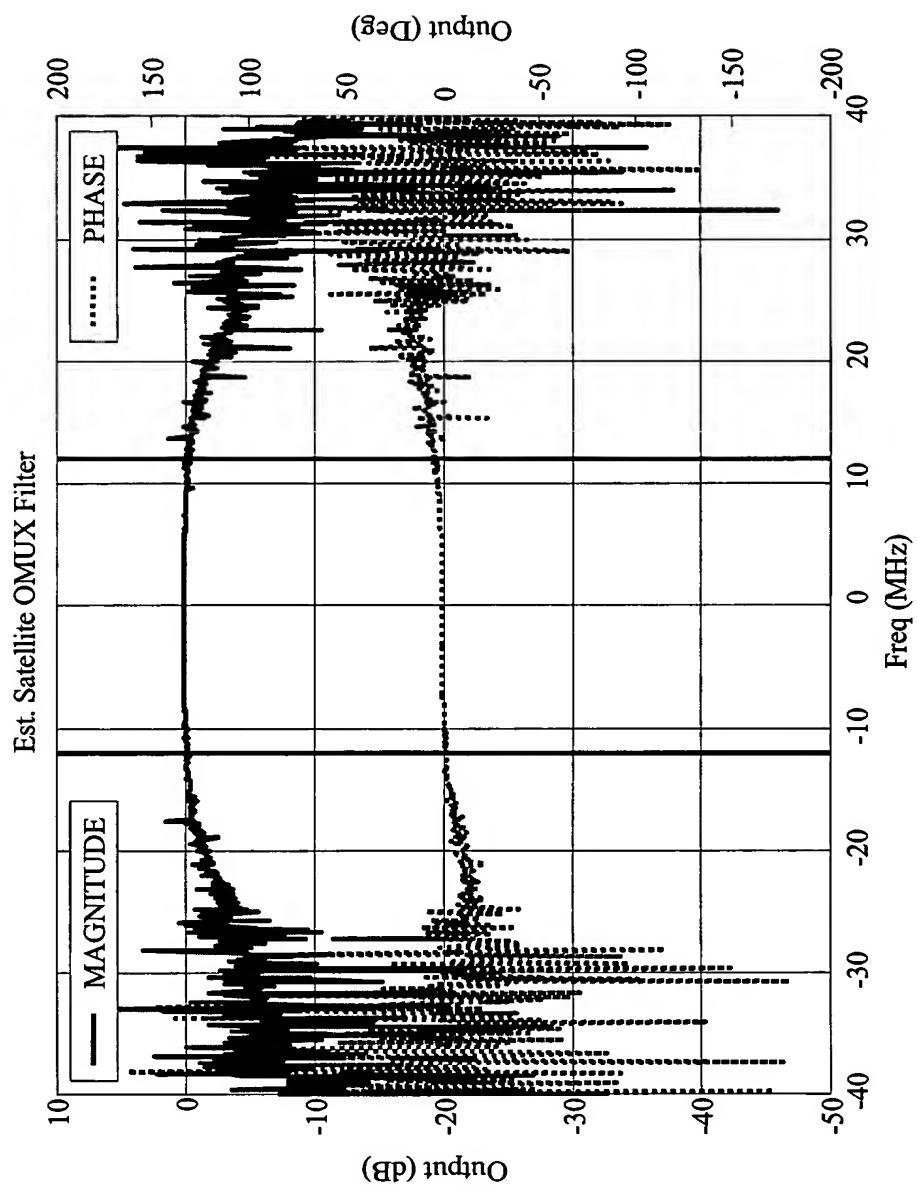


FIG. 16

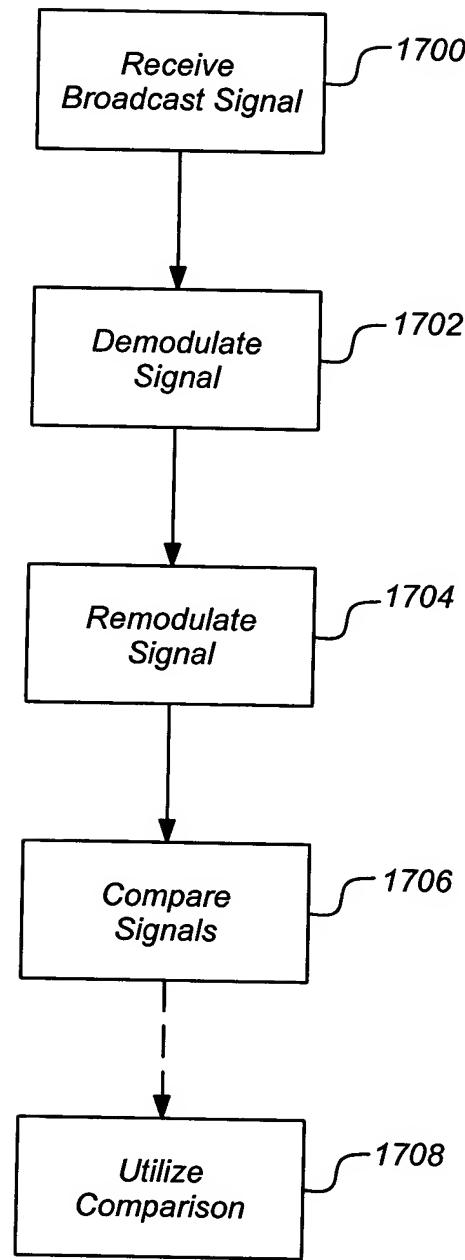


FIG. 17